



UNREG	2	SI3050YVohYVolYVihYVil Y2.4Y0.35Y2.0Y0.8 74LV244YVihYVilYVohYVol Y2.0Y0.8Y2.8Y0.2 MarginY0.4Y0.4Y0.8Y0.4
5V	1	SI3050YVohYVolYVihYVil Y2.4Y0.35Y2.0Y0.8 74LV244YVihYVilYVohYVol Y2.0Y0.8Y2.8Y0.2 MarginY0.4Y0.45Y0.8Y0.6
nRESET	5	nc
DR0SEC	10	nc
TFS0	11	nc
DT0SEC	12	nc
TMR2	32	nc
TMR1	30	nc
TMR0	28	nc
SCK	26	nc
SDA	24	nc

U2 AND BUFFERING

U2 allows us to isolate the very fast BF533 signals from our circuit. The banks of Rs on either side of U2 allow independent adjustment of rise times.

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FOUR PORT FX0/FXS MODULE DAUGHTER CARD FOR BF533 STAMP	
TITLE	
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